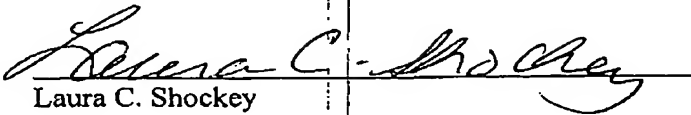


PATENT

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## PATENT COOPERATION TREATY

Applicants : Syougo Murosawa et al.  
Application No. : 10/553,714  
Filed : October 17, 2005  
For : METHOD FOR MANUFACTURING MULTI-LAYERED UNIT  
FOR MULTI-LAYERED CERAMIC ELECTRONIC COMPONENT

Art Unit : 3749  
Docket No. : 890050.533USPC  
Date : November 3, 2006

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Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

REQUEST FOR CORRECTED FILING RECEIPT

Commissioner for Patents:

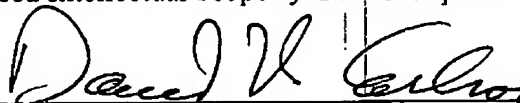
Attached is a copy of the official Filing Receipt received from the PTO in the above-identified application, for which issuance of a corrected Filing Receipt is respectfully requested.

There is an error with respect to the following data, which is incorrectly entered.  
There is an error in title, which should read:

METHOD FOR MANUFACTURING MULTI-LAYERED UNIT FOR MULTI-LAYERED  
CERAMIC ELECTRONIC COMPONENT

The corrections to be made have been marked on the attached copy of the Filing Receipt.

Respectfully submitted,  
Seed Intellectual Property Law Group PLLC



David V. Carlson  
Registration No. 31,153

DVC:lcs

Enclosure:  
Copy of Filing Receipt

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10/553,714	10/17/2005	3749	900	890050.533USPC	13	17	1

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## Applicant(s)

Syugo Murosawa, Tokyo, JAPAN;  
Shigeki Sato, Tokyo, JAPAN;  
Masaaki Kanasugi, Tokyo, JAPAN;

## Assignment For Published Patent Application

- TDK Corporation, Tokyo, JAPAN

Power of Attorney: The patent practitioners associated with Customer Number 500.

## Domestic Priority data as claimed by applicant

This application is a 371 of PCT/JP04/05199 04/12/2004

## Foreign Applications

JAPAN 2003-113833 04/18/2003

Projected Publication Date: 11/02/2006

ENTERED IN DOCKET

Non-Publication Request: No

Early Publication Request: No

Title

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*multi-layered multi-layered ceramic*Method for manufacturing ~~multi-layered~~ unit for ~~multi-layered~~ electronic component**Preliminary Class**

432

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Seed<sup>IP</sup>David V. Carlson  
davec@SeedIP.com*November 3, 2006*  
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